



(19) Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) Publication number: 0 413 991 A1

(12) EUROPEAN PATENT APPLICATION

(21) Application number: 90114495.6

(61) Int. Cl. 5: H05B 41/29

(22) Date of filing: 27.07.90

(30) Priority: 28.07.89 JP 194353/89

(43) Date of publication of application:
27.02.91 Bulletin 91/09

(84) Designated Contracting States:
DE GB

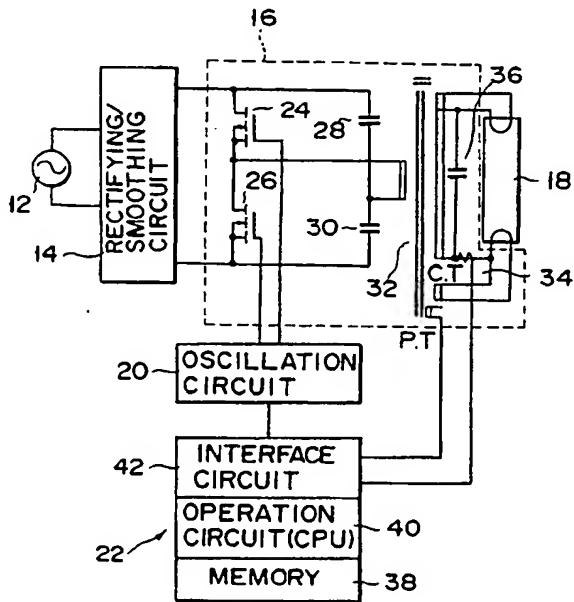
(71) Applicant: TOSHIBA LIGHTING &
TECHNOLOGY CORPORATION
4-28, Mita 1-chome
Minato-ku Tokyo(JP)

(72) Inventor: Kakitani, Tsutomu
A-103 Green-Court-Kanazawabunko, 96
Kanazawa-cho
Kanazawa-ku, Yokohama-shi(JP)

(74) Representative: Henkel, Feiler, Hänel &
Partner
Möhlstrasse 37
D-8000 München 80(DE)

(54) Discharge lamp lighting apparatus for driving discharge lamp according to rating thereof.

(57) A discharge lamp lighting apparatus according to this invention includes an inverter circuit (16) for supplying a high frequency power source to a discharge lamp (18) having one of different ratings, a voltage detecting coil (PT) of an inverter transformer (32) for detecting a load voltage applied from the inverter circuit (16) to the discharge lamp (18), a current transformer for detecting a load current supplied from the inverter circuit (16) to the discharge lamp (18), and a control section (22) and an oscillation circuit (20) for controlling the power source supplying operation of the inverter circuit (16) based on detection outputs of the voltage detection coil (PT) and current transformer (34). The control section (22) and oscillation circuit (20) sequentially raise an output of the inverter circuit (16) at the starting time of the discharge lamp (18), identifies the type of the discharge lamp according to the starting voltage at the lighting time of the discharge lamp (18), and controls the starting voltage and current of the discharge lamp (18) according to the type of the discharge lamp (18) thus identified.



F I G. 1

EP 0 413 991 A1

DISCHARGE LAMP LIGHTING APPARATUS FOR DRIVING DISCHARGE LAMP ACCORDING TO RATING THEREOF

This invention relates to a discharge lamp lighting apparatus, and more particularly, to a discharge lamp lighting apparatus which lights a discharge lamp at a high frequency, can light different types of discharge lamps and can adequately and automatically drive the discharge lamp according to the rating of the discharge lamp.

In general, discharge lamps such as fluorescent lamps include preheat start type lamps and rapid start type lamps which are classified according to the starting type thereof. Such discharge lamps have various ratings and include FL (preheat type lamp) 20 type (20 W), FLR (rapid start type lamp) 40 type (40 W) and FLR 110 type (110 W) and the like for respective outputs. The discharge lamps of the above ratings have different voltage-current characteristic curves and therefore they are driven by different inverter devices which have corresponding load curves. For example, the inverter device for the 110 W discharge lamp is required to generate a starting voltage of approx. 480 V when a load current is 0, and supply a load current of approx. 0.6 A after the discharge lamp is started. Thus, the inverters are required to supply different starting voltages and load currents for respective discharge lamps having different ratings. Therefore, in the prior art, inverter devices are exclusively used for corresponding discharge lamps having different ratings.

However, if different inverter devices are used for respective discharge lamps, the number of types of the discharge lamps increases and it becomes troublesome to use the discharge lamps. Further, it becomes necessary to manufacture various types of inverter devices by a small number for respective types, raising the manufacturing cost of the apparatus and making the apparatus design complicated.

An object of this invention is to provide a highly reliable discharge lamp lighting apparatus which can be commonly used for a plurality of discharge lamps having various different ratings and manufactured at a low cost and in which the design can be simplified.

According to an aspect of the present invention, there is provided a discharge lamp lighting apparatus comprising inverter means for supplying a high frequency power source to a discharge lamp having one of different ratings; voltage detecting means for detecting a load voltage applied from the inverter means to the discharge lamp; and control means for controlling the power source supplying operation of the inverter means with respect to the discharge lamp based on detection

outputs of the voltage detecting means; wherein the control means includes starting voltage changing means for sequentially raising an output of the inverter means at the starting time of the discharge lamp, discharge lamp identifying means for identifying the type of the discharge lamp according to the starting voltage of the discharge lamp, and lighting control means for controlling the starting voltage and current of the discharge lamp according to the type of the discharge lamp determined based on an output of the discharge lamp identifying means.

This invention can be more fully understood from the following detailed description when taken in conjunction with the accompanying drawings, in which:

Fig. 1 is a block diagram schematically showing a discharge lamp lighting apparatus according to this invention;

Fig. 2 is a circuit construction diagram showing an example of discharge lamp lighting apparatus of Fig. 1 more in detail;

Fig. 3 is a graph showing load characteristics corresponding to various discharge lamps and attained by the discharge lamp lighting apparatus of this invention;

Figs. 4A through 4C are timing charts showing variations with time in the secondary voltage of the inverter circuit of the discharge lamp lighting apparatus, lamp current and oscillation frequency;

Fig. 5 is a diagram showing variation in the load characteristic corresponding to the discharge lamp at the starting time of the discharge lamp lighting apparatus;

Fig. 6 is a flowchart for illustrating the operation of the discharge lamp lighting apparatus;

Figs. 7A and 7B are diagrams showing examples of a memory table at the starting time and lighting time of a 40 W discharge lamp;

Fig. 8 is a circuit construction diagram showing another example of discharge lamp lighting apparatus of Fig. 1 more in detail; and

Figs. 9 through 13 are diagrams showing examples of different circuit constructions of the discharge lamp lighting apparatus of Fig. 1.

There will now be described an embodiment of this invention with reference to the accompanying drawings.

Fig. 1 shows a schematic construction of a discharge lamp lighting apparatus according to an embodiment of this invention. In Fig. 1, the discharge lamp lighting apparatus includes an A.C. power source 12, a rectifying/smoothing circuit 14

connected to the A.C. power source 12 to rectify the A.C. power source, an inverter circuit 16 connected to the rectifying/smoothing circuit 14, a discharge lamp 18 which is connected to the inverter circuit 16 and is used as an illumination load such as a fluorescent lamp, an oscillation circuit 20 connected to the inverter 16 and a controlling section 22 for controlling the oscillation circuit 20.

The inverter circuit 16 includes switching transistors 24 and 26 and capacitors 28 and 30 serially connected between D.C. output terminals of the rectifying/smoothing circuit 14. Further, the inverter circuit 16 includes an inverter transformer 32 having a primary coil connected between a connection node between the switching transistors 24 and 26 and a connection node between the capacitors 28 and 30, a current transformer (CT) 34 and a starting capacitor 36.

Further, the control section 22 includes an operation circuit (CPU) 38 constructed by a microprocessor or the like, a memory 40 and an interface circuit 42.

Fig. 2 is a circuit construction diagram showing the discharge lamp lighting apparatus of Fig. 1 more in detail. In Fig. 2, the rectifying/smoothing circuit 14 includes a rectifier circuit 52 connected via transformers 48 and choke coil 50 between two terminals of an A.C. power source 12 with which a semiconductor switch 44 and a capacitor 46 are each connected in parallel and capacitors 54, 56 and 58 connected in parallel with the rectifier circuit. Further, the rectifying/smoothing circuit 14 includes diodes 60, 62 and 64 connected with the polarities as shown in Fig. 2 and capacitors 66 and 68 connected to smooth an output of the rectifier circuit 52.

The inverter circuit 16 includes transistors 70 and 72 controlled by an output of the oscillation circuit 20, resistors 74, 76, 78, 80, 82, 84 and 86, a zener diode 88 and capacitors 90 and 92 in addition to the transistors 24 and 26 and capacitors 28 and 30 shown in Fig. 1.

The control section 22 includes A/D converters 94 and 96 for A/D converting signals from the voltage detection coil PT of the inverter transformer 32 and the current transformer 34, a D/A converter 98 for D/A converting an output of the CPU 40, a resistor 100 and capacitors 102 and 104 in addition to the memory 38, CPU 40 and interface circuit 42.

The oscillation circuit 20 includes a V/F converter 106 for effecting the voltage-frequency conversion, for example. Further, the oscillation circuit 20 includes a comparator 108, D.C. power sources 110 and 112, diode 114, resistors 116, 118, 120, 122, 124 and 126 and a capacitor 128 which are connected as shown in Fig. 2.

In the apparatus of Fig. 2, when the A.C. power source 12 is turned on, an A.C. voltage is con-

verted into a D.C. voltage by the rectifying/smoothing circuit 14 and then applied to the inverter circuit 16. In the inverter circuit 16, the switching transistors 24 and 26 are alternately turned on and off by a high-frequency signal supplied from the oscillation circuit 20. Then, a voltage of the high frequency power source is applied to the primary coil of the inverter transformer 32. As a result, the discharge lamp 18 is preheated by the action of the capacitor 36 as is well known in the art. After this, when a voltage between two electrodes of the discharge lamp 18 becomes higher than the starting voltage, the discharge lamp 18 is lighted.

When the discharge lamp 18 is lighted, signals corresponding to a current flowing in the discharge lamp 18 and a voltage applied thereto are respectively derived by means of the current transformer (CT) 34 and the voltage detection coil PT of the inverter transformer 32 and input to the CPU 40 via the interface 42.

The CPU 40 detects the rating of the discharge lamp 18 based on the starting voltage of the discharge lamp 18 as will be described later in detail. Then, it causes a power source voltage corresponding to the rating of the discharge lamp 18 to be output from the inverter circuit 16 and controls the succeeding lighting operation. In this case, the output adjustment of the inverter circuit 16 is effected by adjusting the oscillation frequency of the oscillation circuit 20, that is, an output frequency of the V/F converter 106 which changes an output frequency according to the input voltage. At this time, if the oscillation frequency becomes higher, the output of the inverter circuit 16 is lowered, and if the oscillation frequency becomes lower, the output of the inverter circuit 16 is raised. The control section 22 controls the load curve of the inverter circuit 16 by making use of the characteristic so as to cause the same to correspond to the rating of the discharge lamp 18.

Fig. 3 is a graph showing the load characteristics corresponding to different types of discharge lamps of 20 W type, 40 W type and 110 W type, for example. The output voltage and current of the inverter circuit 16 are controlled by the CPU 40 according to the type of the discharge lamp 18.

Next, the operation of this invention is explained with reference to the graph of Fig. 3 showing the load characteristics, the timing charts shown in Figs. 4A through 4C, the load characteristic diagram of Fig. 5 corresponding to the discharge lamp at the starting time and the flowchart shown in Fig. 6.

For example, assume that the discharge lamp 18 of 40 W (FLR 40) is mounted on the apparatus of Fig. 1. In this case, when the A.C. power source 12 is turned on, the CPU 40 of the control section

22 sets the initial value of an output voltage of the inverter circuit 16 or the secondary voltage (V_{20}) of the inverter transformer 32 in the step ST1. For example, the initial value is set at 100 V at time t_0 as shown in Fig. 4A. Then, the secondary voltage is raised from the initial voltage of 100 V by 10 V at one time in the step ST2. In the step ST3, it is determined whether or not the secondary voltage V_{20} is lower than 600 V, for example. At this time, if the secondary voltage V_{20} is not lower than 600 V, it is determined that an abnormal condition has occurred and the step ST4 is effected to interrupt the oscillation of the primary side of the inverter transformer 32.

In contrast, if it is detected in the step ST3 that the secondary voltage V_{20} is lower than 600 V, the step ST5 is effected to determine whether the lamp current is present or not based on the output of the current transformer 34. After this, in the step ST6, it is determined whether the discharge lamp 18 is lighted or not based on the detection result of the lamp current. If it is detected in the step ST6 that the discharge lamp 18 is not lighted, the step ST2 is effected again to raise the secondary voltage V_{20} by 10 V. In this way, the secondary voltage V_{20} of the inverter transformer 32 is raised by 10 V at one time as shown in Fig. 4A, for example, and then the turn-on of the discharge lamp 18 is monitored (time t_0 to t_1). Then, if the lamp current (0.1 A) is detected in the step ST5 at time t_1 as shown in Fig. 4B, for example, and the discharge lamp 18 is lighted in the step ST6, the step ST7 is effected to determine the value of the starting voltage (V_{20}) at the lighting time of the discharge lamp 18. In this case, since the discharge lamp of FLR 40 type (40 W) is mounted, the secondary voltage V_{20} at the time t_1 has reached 300 V which is an approximate average voltage of the starting voltage of the FLR 40 type discharge lamp.

If the starting voltage V_{20} determined in the step ST7 is lower than 200 V, it is determined that an FL 20 type or 20 W discharge lamp is mounted and a lighting routine for the FL 20 type discharge lamp is effected to keep the operation condition of the discharge lamp 18 in a proper range. Further, if it is determined in the step ST7 that the starting voltage V_{20} is not lower than 200 V, then the step ST8 is effected to determine whether or not the starting voltage V_{20} is lower than 350 V. If it is determined in the step ST8 that the starting voltage V_{20} is not lower than 200 V and is lower than 350 V, it is determined that an FLR 40 type or 40 W discharge lamp is mounted and a lighting routine for the FLR 40 type discharge lamp is effected (after time t_2). If it is determined in the step ST8 that the starting voltage V_{20} is not lower than 350 V, it is determined that an FLR 110 type or 110 W discharge lamp is mounted. Therefore, a lighting

routine for the FLR 110 type discharge lamp is effected.

Each of the lighting routines is effected to supply a current corresponding to a lamp current value previously stored in the memory 38 of the control section 22 according to the type of the discharge lamp and maintain the lighted state.

Figs. 7A and 7B are diagrams showing examples of tables of the memory at the starting time and lighting time of a 40 W discharge lamp. For example, at the starting time shown in Fig. 7A, values of lamp current I_L are stored in the unit of 0.01 A, values of starting voltage V_{20} are stored in the unit of 10 V, and values of oscillation frequency f are stored in the unit of 0.5 KHz. Likewise, at the starting time shown in Fig. 7B, values of lamp current I_L are stored in the unit of 0.01 A, values of starting voltage V_{20} are stored in the unit of 10 V, and values of oscillation frequency f are stored in the unit of 0.5 KHz. Thus, items of characteristic data of the load curves corresponding to lamps of respective ratings are stored in the memory 38. After the discharge lamp 18 is lighted, the output of the inverter circuit 16 is maintained at a proper operation point according to the above characteristic data.

In the above explanation, the discharge lamp lighting apparatus is a half-bridge type, but it is not limited to this type and a full-bridge type or other type lighting apparatus can be used, for example.

Further, fluorescent lamps of FL 20 type, FLR 40 type and FLR 110 type are used as the discharge lamp, but it is not limited to them. In addition, the lamp ratings other than the above ratings can be used, and ring-form FCL lamps, U-shaped FPL lamps can be used, for example.

Further, in the above example, a discharge lamp is mounted on one discharge lamp lighting apparatus, but discharge lamps connected in series or discharge lamps connected in parallel can be used.

Fig. 8 is a circuit construction diagram showing the discharge lamp lighting apparatus of Fig. 1 more in detail. In Fig. 8, portions which are the same as those of Fig. 3 are denoted by the same reference numerals, and only portions which differ from those of Fig. 3 are explained.

That is, the rectifying/smoothing circuit 14 and inverter circuit 16 are formed with the same constructions of Fig. 3. A control section 22' is formed with a construction which can be obtained by omitting the D/A converter 98 from the control section 22 of Fig. 3. An oscillation circuit 20' is constructed to receive an output of the CPU 40 at an interface circuit 43 and then supply the same to an inverter circuit 16 via inverter circuit 132 and buffer circuit 134.

With the discharge lamp lighting apparatus of

the above construction, the output frequency of the oscillation circuit 20' can be adjusted by changing the number of clocks in the CPU 40. The control section 22' controls the load curve of the inverter circuit 16 by making use of the above property so as to cause the same to correspond to the rating of the discharge lamp 18.

Figs. 9 through 13 are diagrams showing examples of different circuit constructions of the inverter circuit of Fig. 1. In the circuit construction diagrams of Figs. 9 through 13, portions which are the same as those in the former embodiment are denoted by the same reference numerals and only different portions are explained.

Fig. 9 shows an example of a full-bridge type discharge lamp lighting apparatus which includes two switching transistors 25 and 27 arranged in an inverter circuit 16, in addition to the switching transistors 24 and 26. The switching transistors 24, 25, 26 and 27 are controlled by a driving circuit 21 which receives a signal from the interface circuit 42 of the control section 22. With the above construction, the load on the switching transistors can be alleviated in comparison with the half-bridge type discharge lamp lighting apparatus shown in Fig. 1. Therefore, it is suitable for a load of large capacity.

Fig. 10 shows an example of a discharge lamp lighting apparatus constructed by modifying a half-bridge type discharge lamp lighting apparatus and obtained by omitting the voltage dividing capacitors 28 and 30 from the inverter circuit 16 of the discharge lamp lighting apparatus shown in Fig. 1 and connecting a capacitor 29 between the primary coil of the inverter transformer 32 and a connection node between the switching transistors 24 and 26. The switching transistors 24 and 26 are controlled by the driving circuit 21.

Fig. 11 shows an example of a one-transistor type inverter circuit 163 in which a resonance capacitor 136 is connected in parallel with the primary coil of the inverter transformer 32 and a starting capacitor 138 is connected between two terminals of the discharge lamp 18. Further, a parallel circuit of a switching transistor 140 and a diode 142 is connected in series with the primary coil of the inverter transformer 32. The switching transistor 140 is controlled by a duty controller 144. The duty controller 144 controls the switching transistor 140 according to an output of an OR circuit 146 to which outputs of the current transformer (CT) 34 and the voltage detection coil of the inverter transformer 32 are input.

Fig. 12 shows an example of a direct-coupled type half-bridge inverter circuit 164 in which the current transformer 34 is connected to a connection node between the switching transistors 24 and 26 via a choke coil 148. A starting capacitor 138 is connected between the two terminals of the dis-

charge lamp 18. Thus, the inverter transformer 32 which is used in the former embodiment is not used in the inverter circuit 164, making it possible to reduce the whole size of the apparatus.

Fig. 13 shows an example in which a ballast is used in the inverter circuit and two discharge lamps are serially connected. In Fig. 13, a ballast 150 is connected as a different inductance between the primary coil of the inverter transformer 32' and a connection node between the switching transistors 24 and 26 and a capacitor 152 is connected in parallel with the primary coil. In addition, two discharge lamps 18₁ and 18₂ are serially connected on the secondary coil side of the inverter transformer 32'. With this construction, the total size of the discharge lamp lighting apparatus for one discharge lamp can be reduced and the design for the inverter transformer can be simplified.

Claims

1. A discharge lamp lighting apparatus comprising inverter means for supplying a high frequency power source to a discharge lamp having one of different ratings; voltage detecting means for detecting a load voltage applied from said inverter means to said discharge lamp; and control means for controlling the power source supplying operation of said inverter means with respect to said discharge lamp based on detection outputs of said voltage detecting means; characterized in that said control means (22) includes starting voltage changing means (42, 40) for sequentially raising an output of said inverter means (16) at the starting time of said discharge lamp (18), discharge lamp identifying means (38, 40) for identifying the type of said discharge lamp according to the starting voltage of said discharge lamp (18), and lighting control means (40) for controlling the starting voltage and current of said discharge lamp (18) according to the type of said discharge lamp (18) determined based on an output of said discharge lamp identifying means (38, 40).
2. An apparatus according to claim 1 characterized in that said lighting control means (40) includes frequency varying means (20) for changing the frequency according to the type of said discharge lamp (18) determined based on an output of said discharge lamp identifying means (38, 40) to control the power source supplying operation of said inverter means (16).
3. An apparatus according to claim 2, characterized in that said frequency varying means (20) includes a voltage-frequency converter (106) for converting an output frequency according to an output of said discharge lamp identifying means (38, 40).

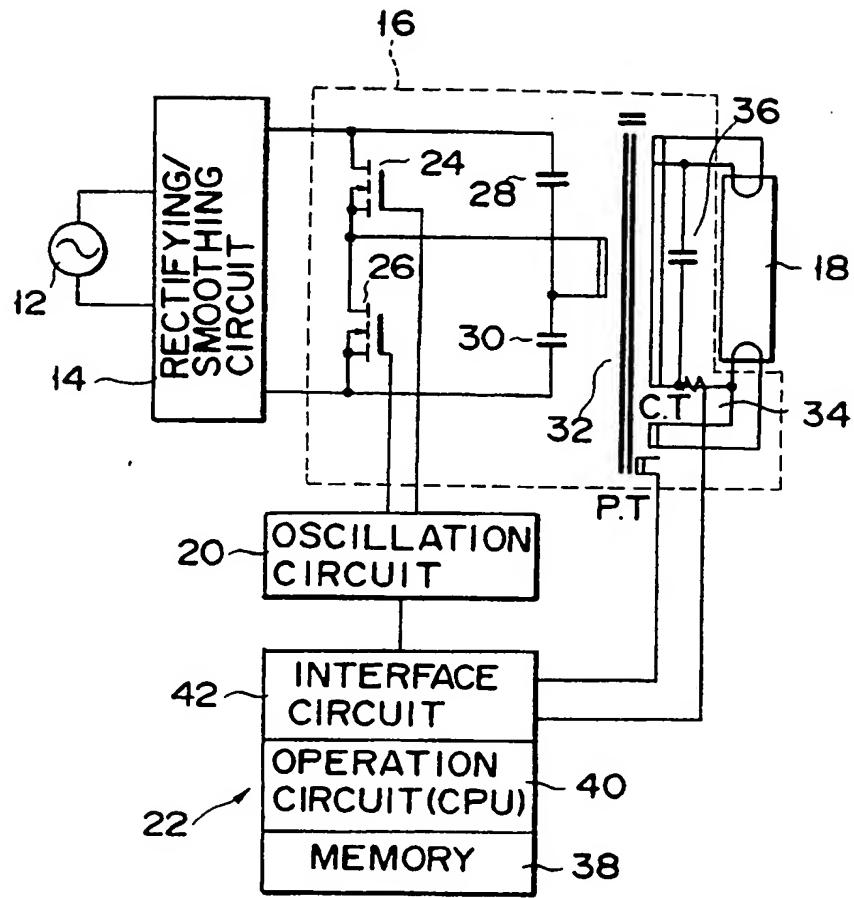
4. An apparatus according to claim 1, characterized in that said lighting control means (40) includes memory means (38) in which load characteristics corresponding to the types of discharge lamps (18) are stored and said lighting control means (40) controls the lighting voltage and current of said discharge lamp (18) according to the load characteristic stored in said memory means (38) and corresponding to the type of said discharge lamp (18) determined based on an output of said discharge lamp identifying means (38, 40). 5
5. An apparatus according to claim 1, characterized in that said starting voltage changing means (42, 40) sequentially raises an output of said inverter means (16) from a preset initial value by a preset amount at one time until said discharge lamp (18) is lighted. 10
6. An apparatus according to claim 1, characterized in that said control means (22) further includes clock generation means (40) and controls the power source supply operation of said inverter means (16) by changing the frequency according to the type of said discharge lamp (18) determined based on an output of said discharge lamp identifying means (38, 40) and according to variation in the number of clocks output from said clock generating means (40) in a preset period of time. 15
7. An apparatus according to claim 1, characterized by further comprising current detecting means (34) for detecting a load current supplied from said inverter means (16) to said discharge lamp (18), characterized in that said control means (22) controls the power source supplying operation of said inverter means (16) with respect to said discharge lamp (18) based on detection outputs of said voltage detecting means (PT) and said current detecting means (34). 20
- 25
- 30
- 35

40

45

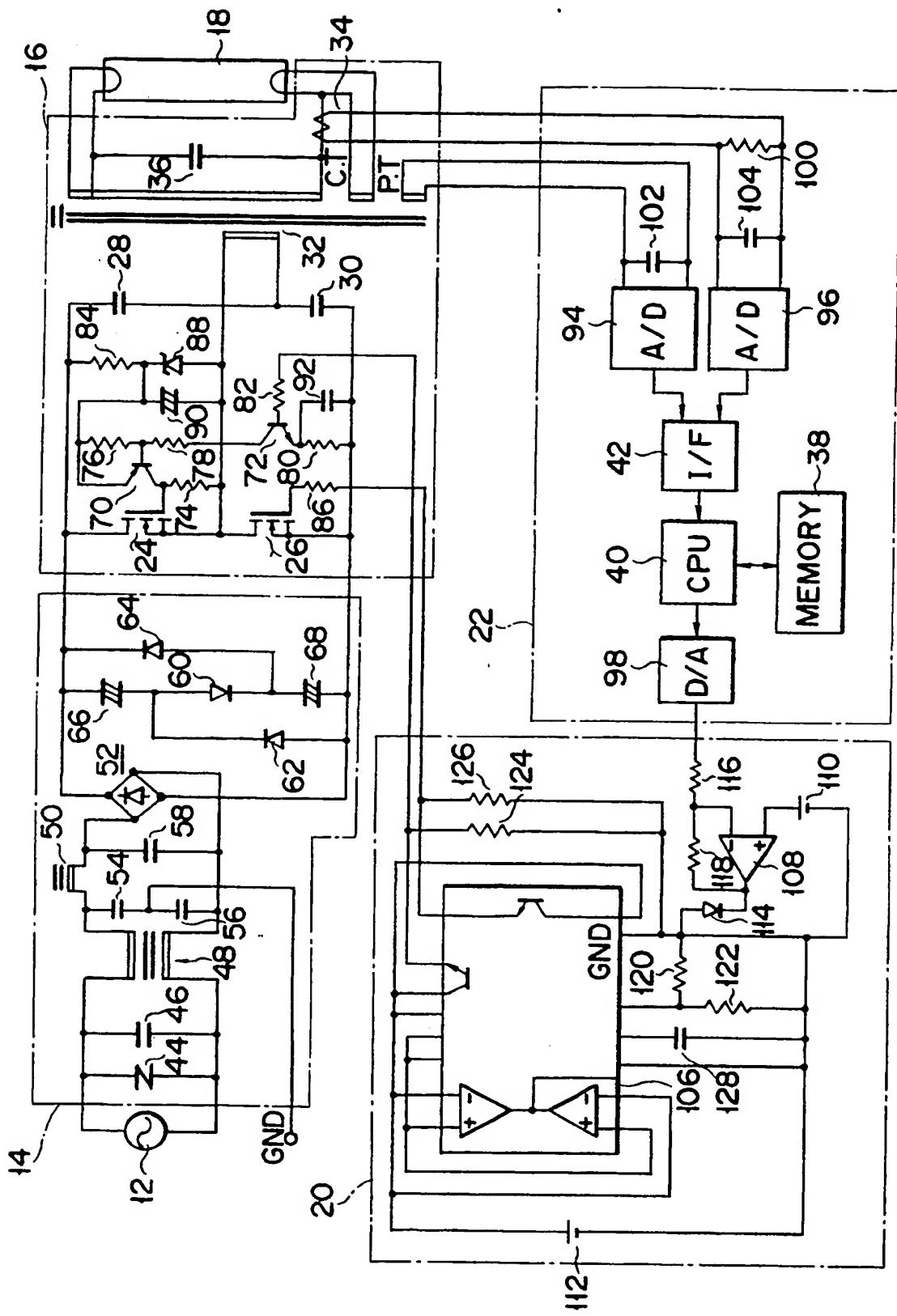
50

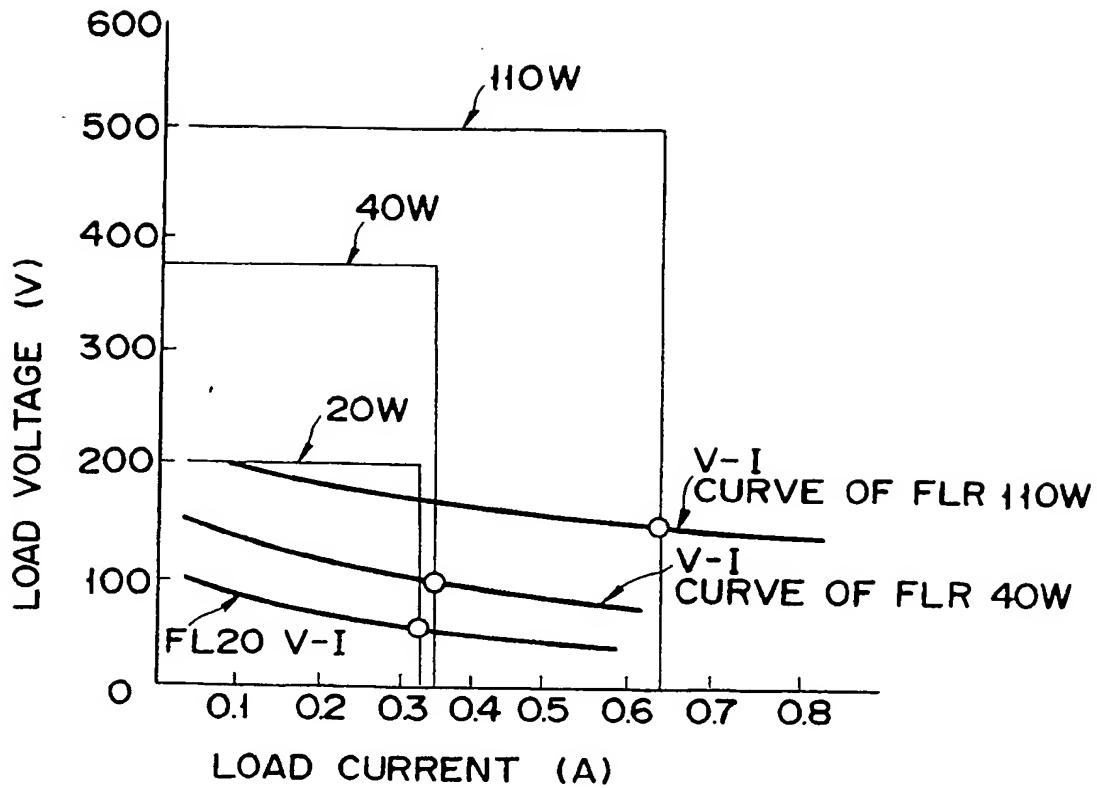
55



F I G. 1

FIG. 2





F I G. 3

FIG. 4A

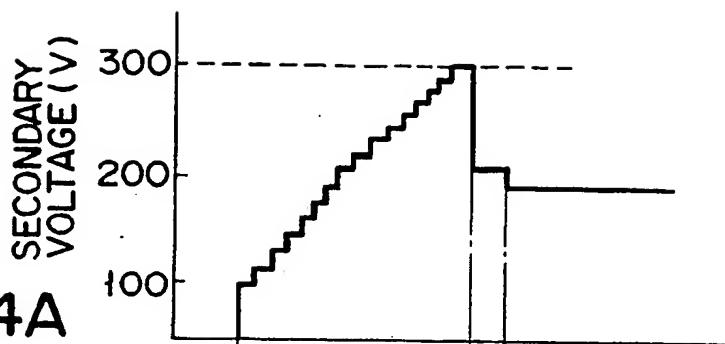


FIG. 4B

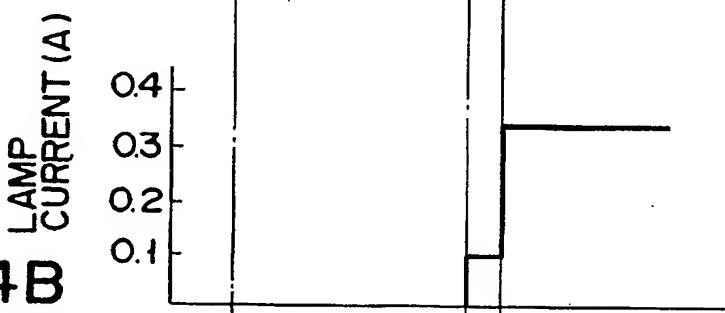
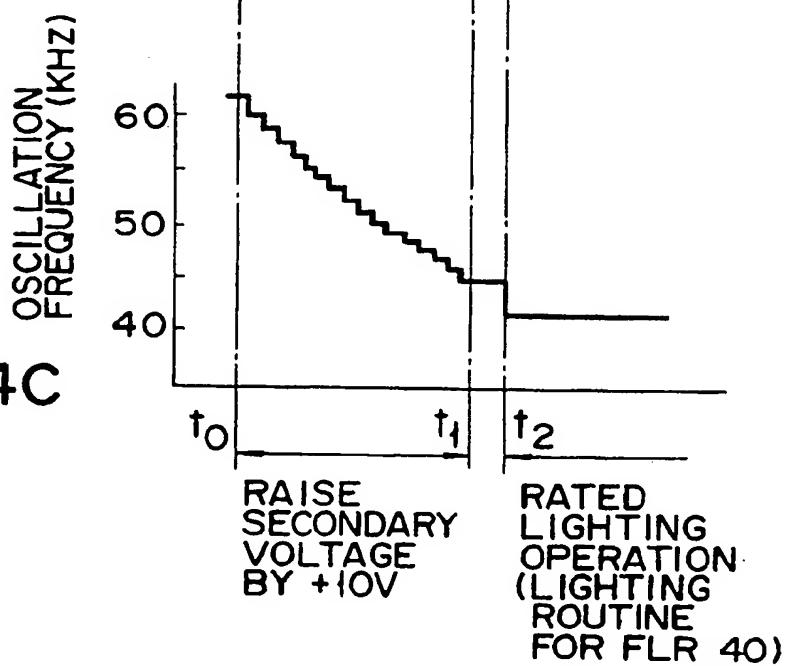


FIG. 4C



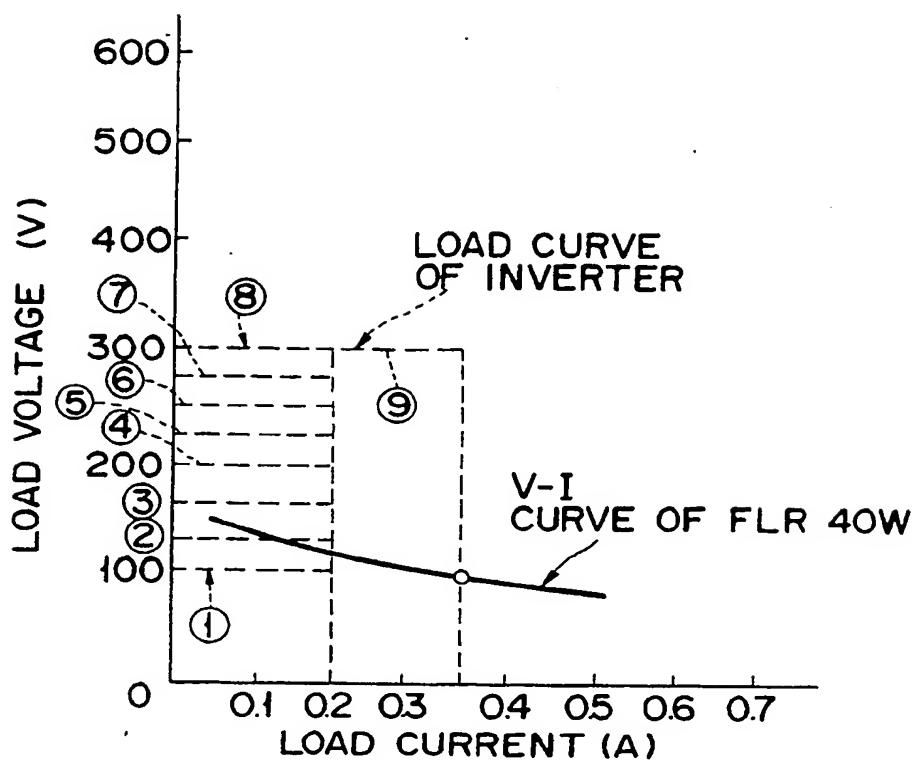


FIG. 5

I_L (A)	V_{20} (V)	f (KHZ)
0	100	60
0.01	100	59.5
0.02	100	59
0.10	100	55
0.10	90	55.5
0.10	80	56
0.10	0	60
0	110	59.5
0.01	110	59
0.02	110	58.5
0.10	110	54.5
0.10	100	55
0.10	90	55.5
0.10	0	60

FIG. 7A

I_L (A)	V_{20} (V)	f (KHZ)
0	300	48
0.01	300	47.5
0.02	300	47
0.34	300	42
0.34	290	42.5
0.34	280	43
0.34	200	45
0.34	0	50

FIG. 7B

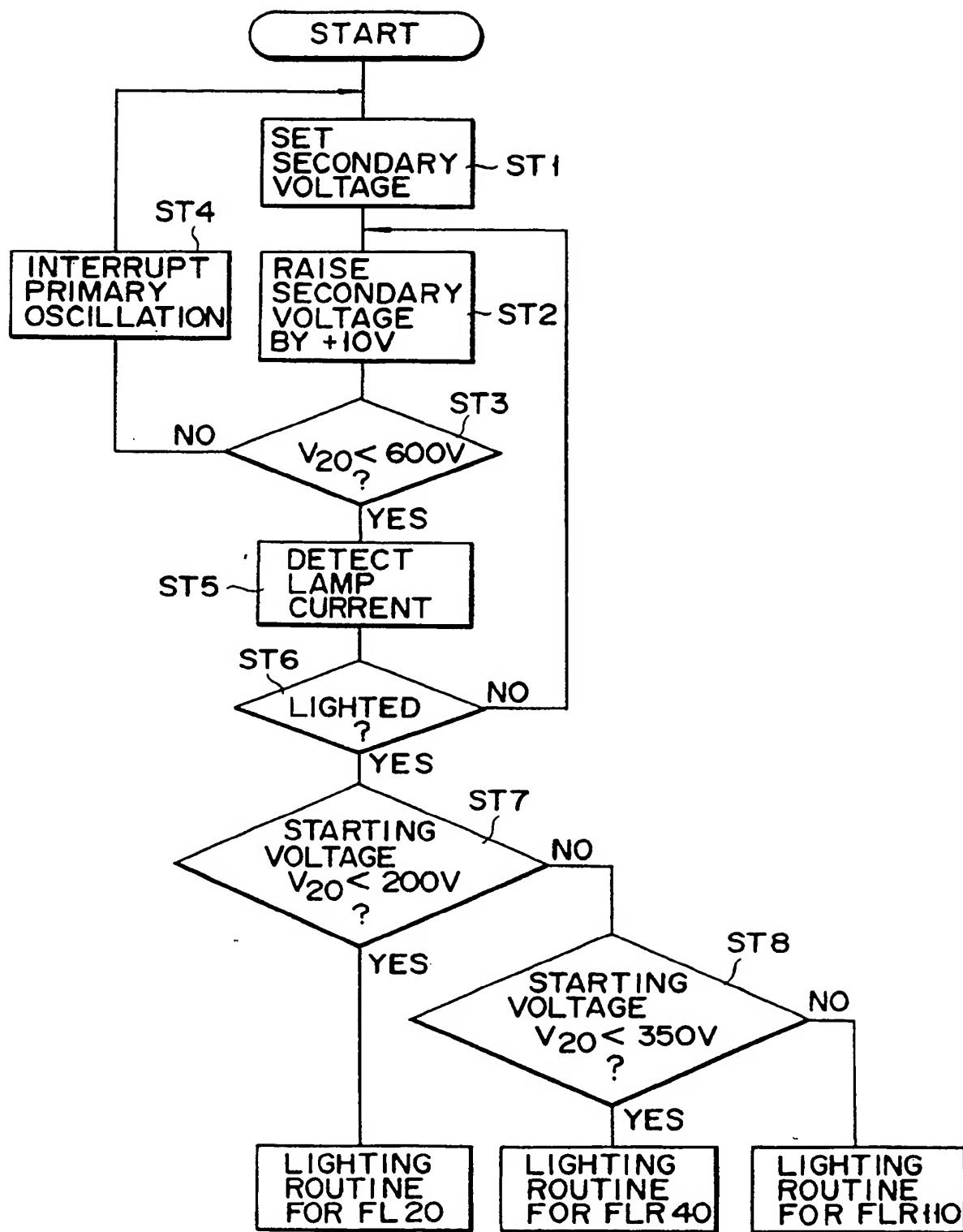
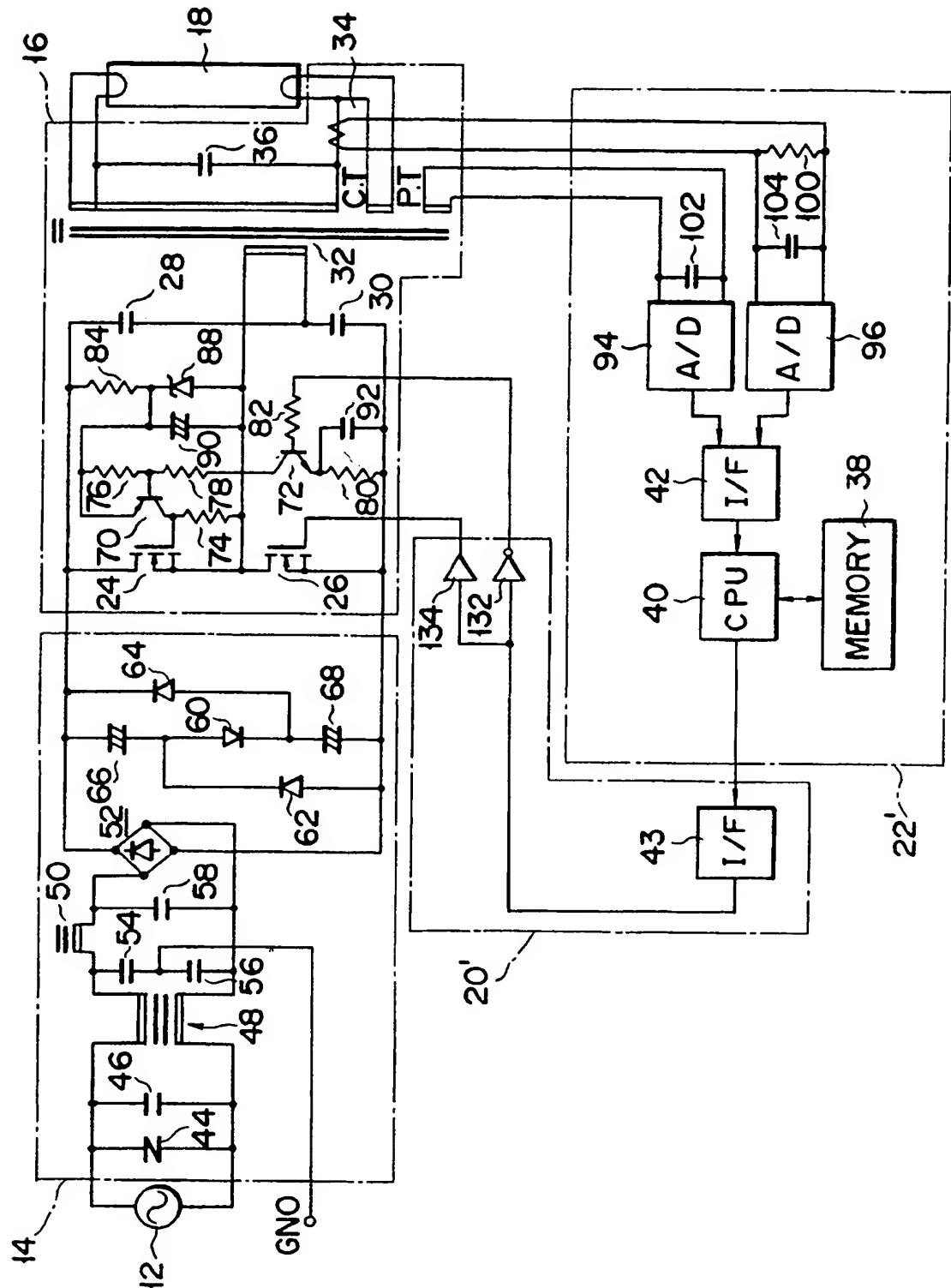


FIG. 6



8-FIG

FIG. 9

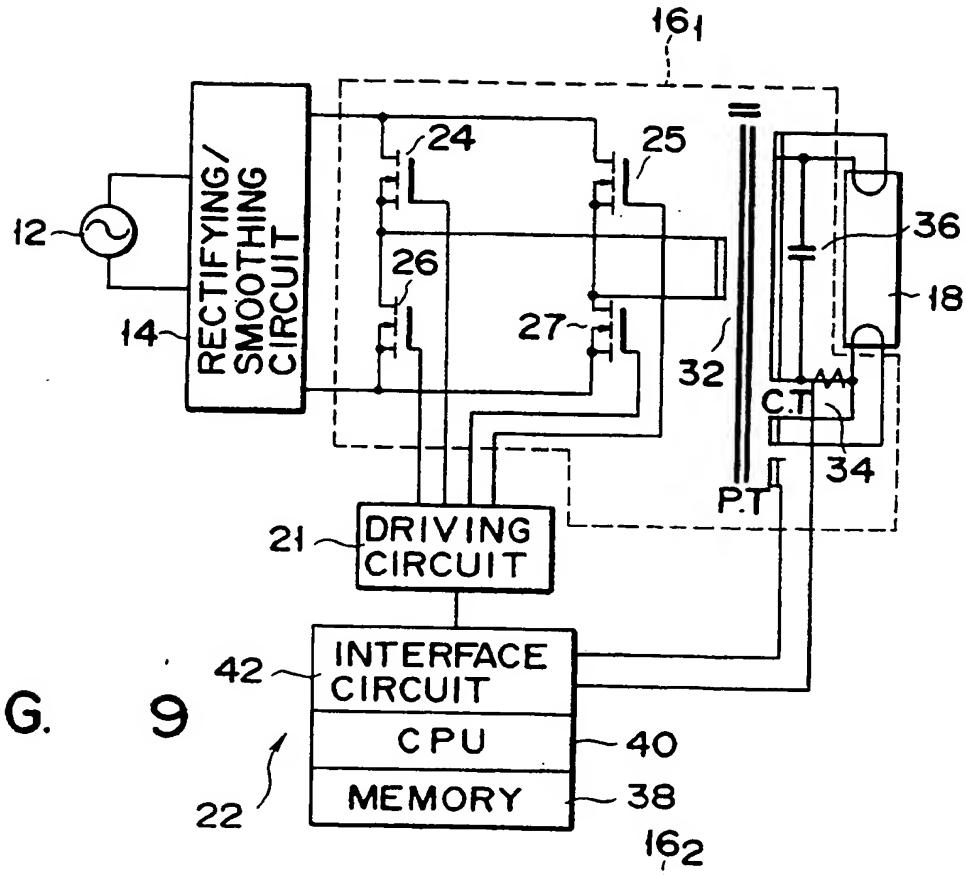
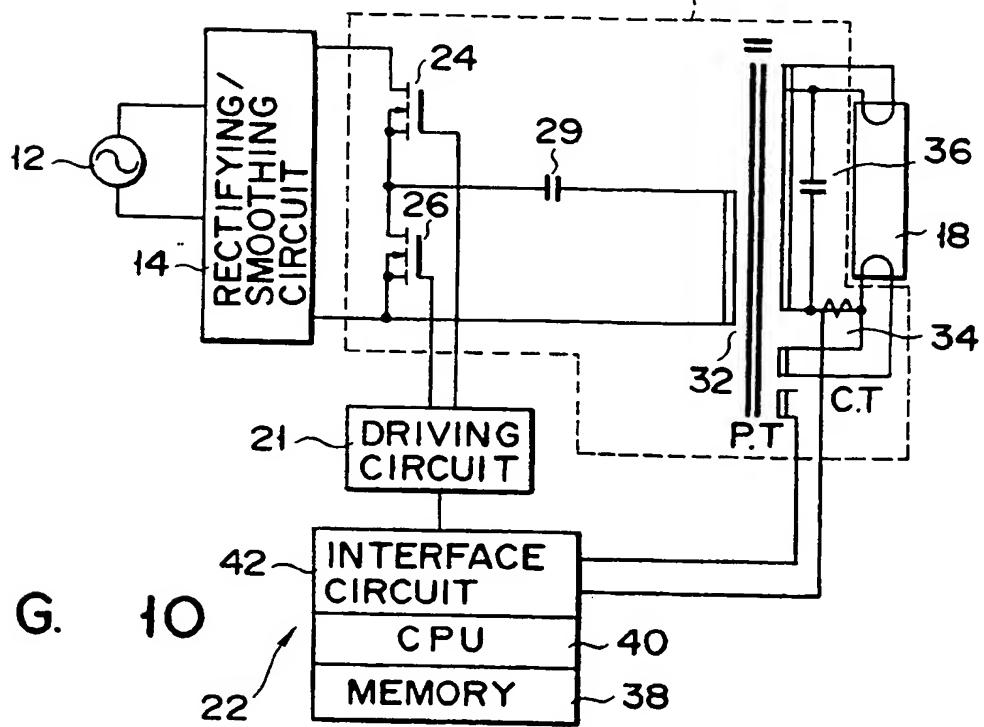
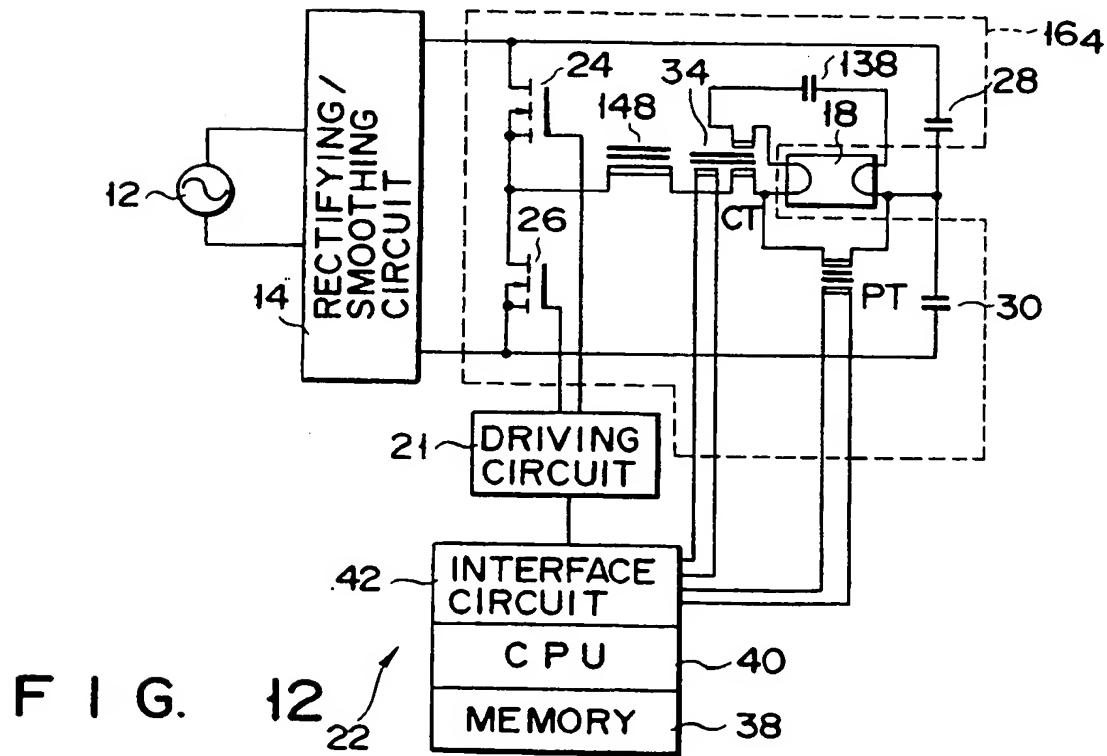
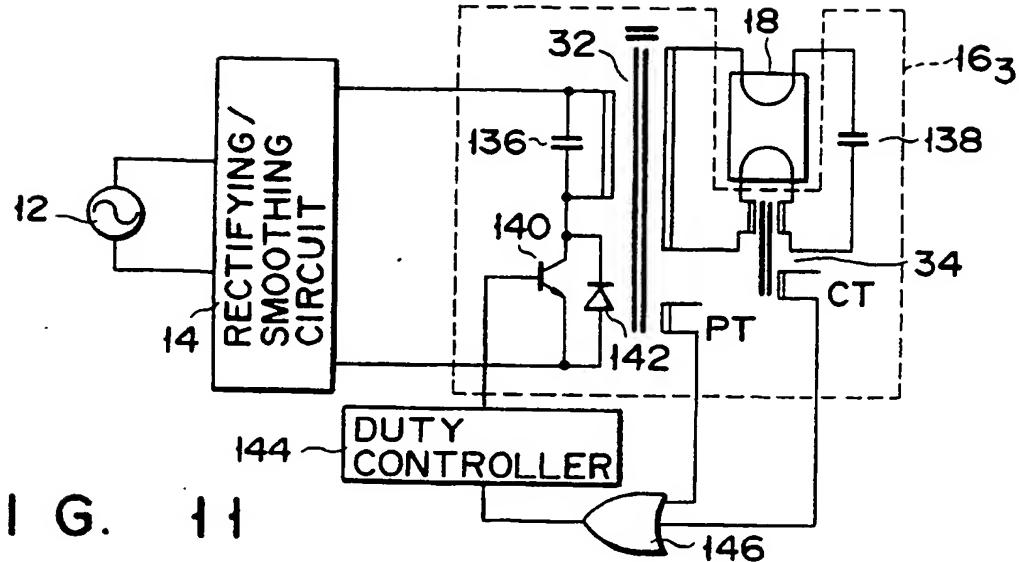
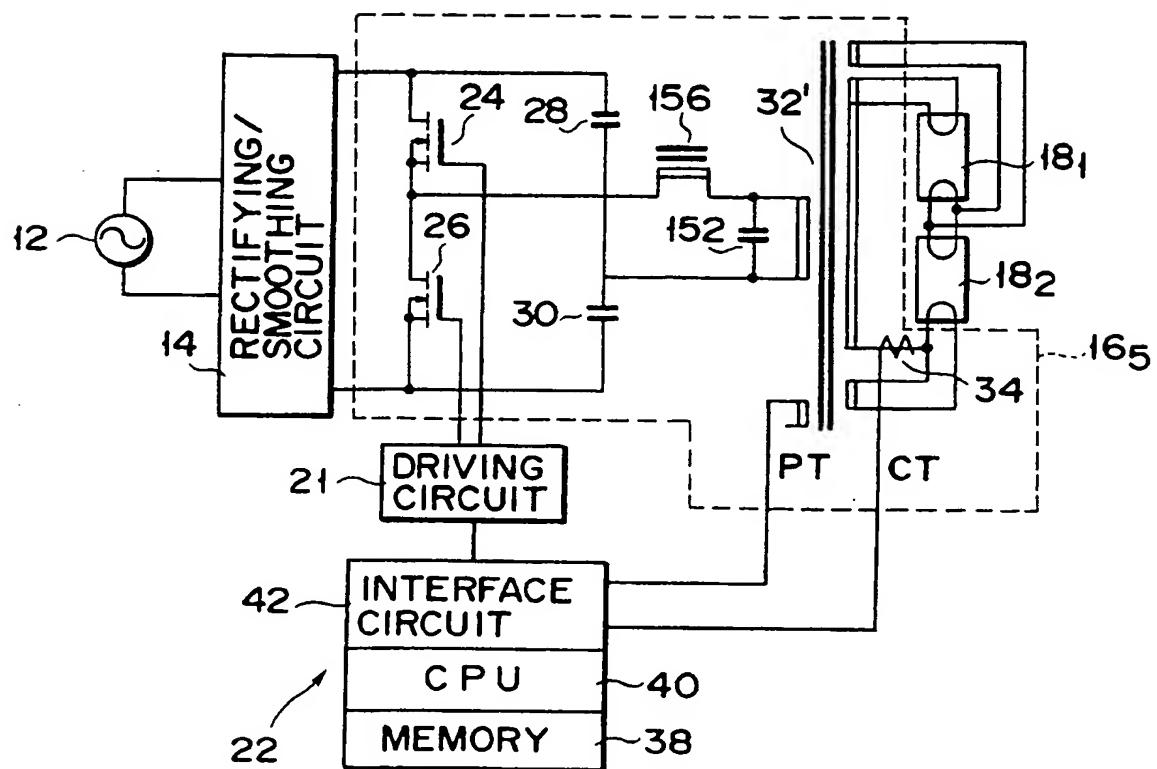


FIG. 10







F I G. 13



European
Patent Office

EUROPEAN SEARCH
REPORT

Application Number

EP 90 11 4495

DOCUMENTS CONSIDERED TO BE RELEVANT					
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)		
A	EP-A-0 127 101 (SIEMENS) * page 2, line 2 - page 2, line 22 ** page 4, line 9 - page 4, line 26; figures 1, 2 * - - -	1	H 05 B 41/29 H 05 B 41/38		
A	EP-A-0 241 279 (ACTRONIC) * page 12, line 24 - page 13, line 23; figure 4 * - - - -	1			
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int. Cl.5)		
			H 05 B		
Place of search	Date of completion of search	Examiner			
The Hague	11 December 90	SPEISER P.			
CATEGORY OF CITED DOCUMENTS					
X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: Intermediate document T: theory or principle underlying the invention					
E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document					